



UNI-SEMICONDUCTOR CO., LTD

宇力半导体有限公司



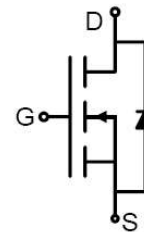
AP30H50Q Data Sheet

V 1.1

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Feature

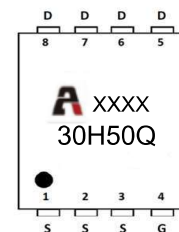
- 30V,40A
 $R_{DS(ON)} < 9m\Omega @ V_{GS}=10V$ TYP=6.5 m Ω
 $R_{DS(ON)} < 14m\Omega @ V_{GS}=4.5V$ TYP=9.0 m Ω
- Advanced Trench Technology
- Lead free product is acquired
- Excellent $R_{DS(ON)}$ and Low Gate Charge



Schematic Diagram

Application

- PWM applications
- Load Switch
- Power management



Marking and pin Assignment

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
30H50Q	AP30H50Q	PDFN3X3-8L	13 inch	-	5000

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_a=25^\circ\text{C}$)	I_D	40	A
Continuous Drain Current ($T_a=100^\circ\text{C}$)	I_D	28	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	150	A
Single Pulsed Avalanche Energy ⁽²⁾	E_{AS}	39	mJ
Power Dissipation	P_D	30	W
Thermal Resistance from Junction to Case ⁽⁴⁾	$R_{\theta JC}$	3.72	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~ +150	$^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage ⁽³⁾	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2.5	V
Drain-source on-resistance ⁽³⁾	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 15A$	-	6.5	9.0	m Ω
		$V_{GS} = 4.5V, I_D = 10A$	-	9.0	14	
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$	-	1116	-	pF
Output Capacitance	C_{oss}		-	187	-	
Reverse Transfer Capacitance	C_{rss}		-	152	-	
Switching characteristics						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 15V, I_D = 15A,$ $V_{GS} = 10V, R_G = 3\Omega$	-	15	-	ns
Turn-on rise time	t_r		-	19	-	
Turn-off delay time	$t_{d(off)}$		-	35	-	
Turn-off fall time	t_f		-	21	-	
Total Gate Charge	Q_g	$V_{DS} = 15V, I_D = 15A,$ $V_{GS} = 10V$	-	13.3	-	nC
Gate-Source Charge	Q_{gs}		-	3.1	-	
Gate-Drain Charge	Q_{gd}		-	5	-	
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V_{DS}	$V_{GS} = 0V, I_S = 1A$	-	-	1.2	V
Diode Forward current ⁽⁴⁾	I_S		-	-	40	A

Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition: $T_J = 25^\circ\text{C}, V_{DD} = 15V, R_G = 25\Omega, L = 0.5mH, I_{AS} = 12.6A$
3. Pulse Test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
4. Surface Mounted on FR4 Board, $t \leq 10$ sec

Test Circuit

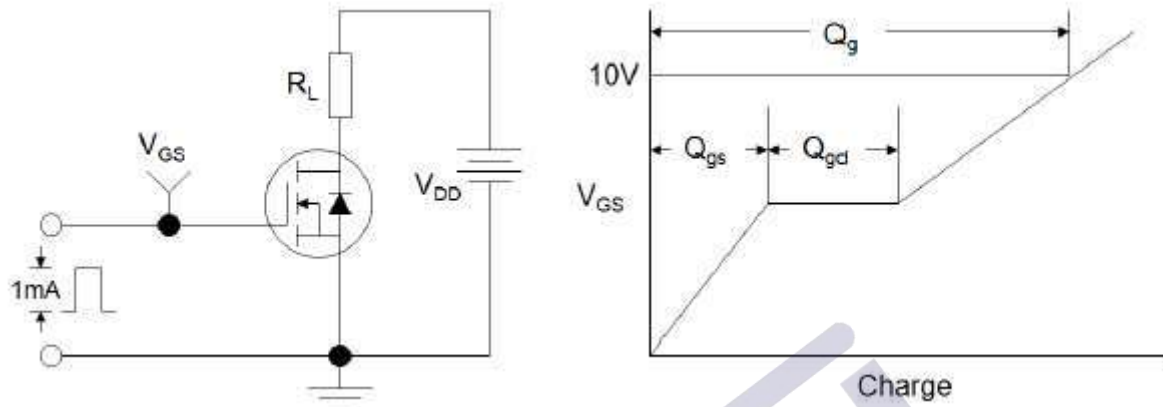


Figure 1: Gate Charge Test Circuit & Waveform

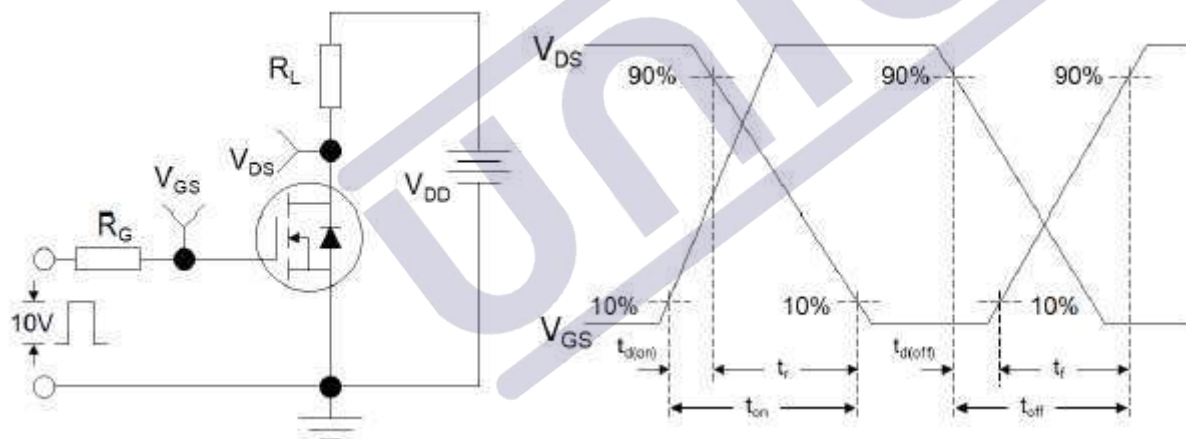


Figure 2: Resistive Switching Test Circuit & Waveforms

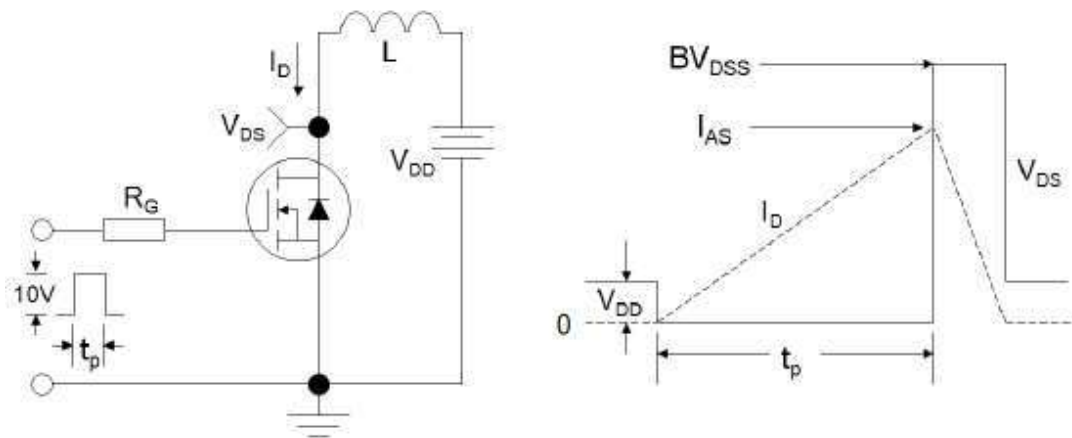


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

Typical Performance Characteristics

Figure 1: Output Characteristics

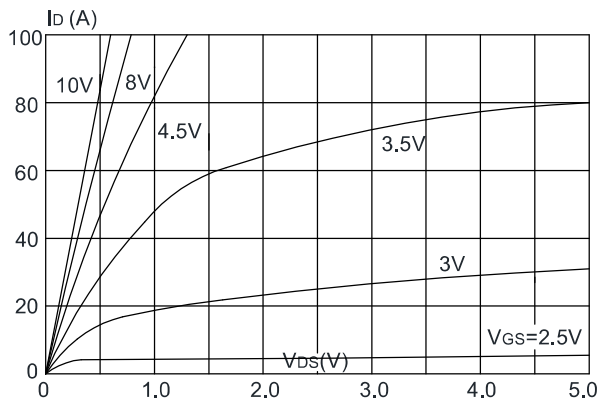


Figure 2: Typical Transfer Characteristics

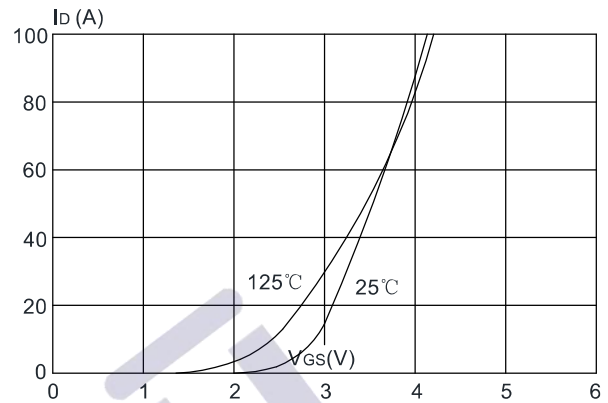


Figure 3: On-resistance vs. Drain Current

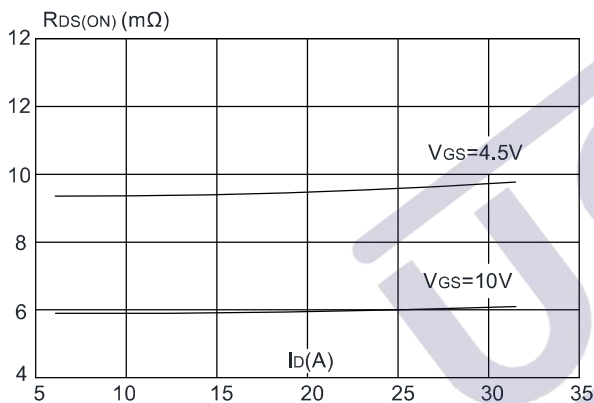


Figure 4: Body Diode Characteristics

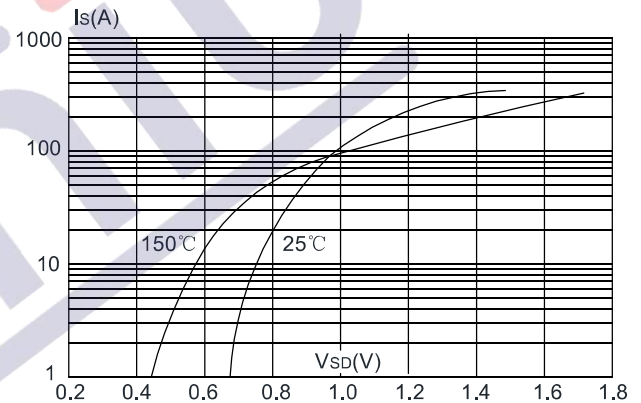


Figure 5: Gate Charge Characteristics

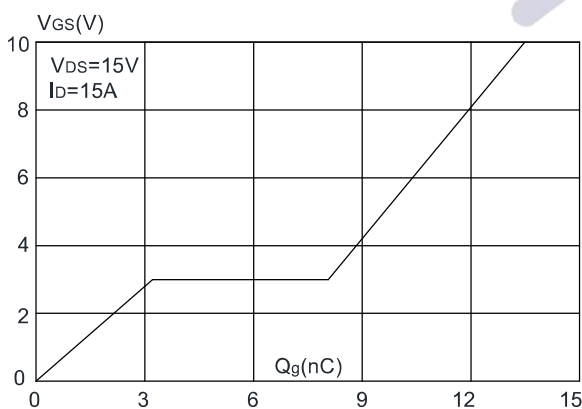


Figure 6: Capacitance Characteristics

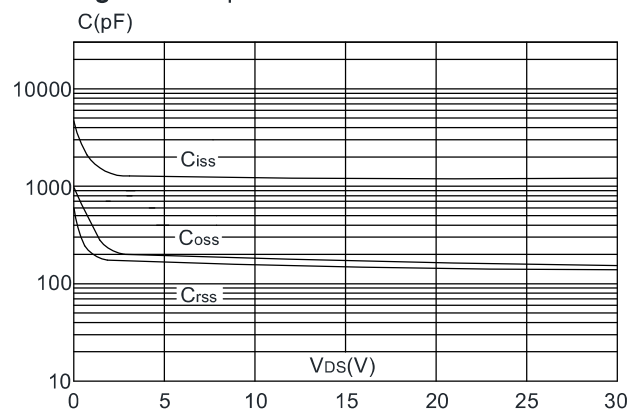


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

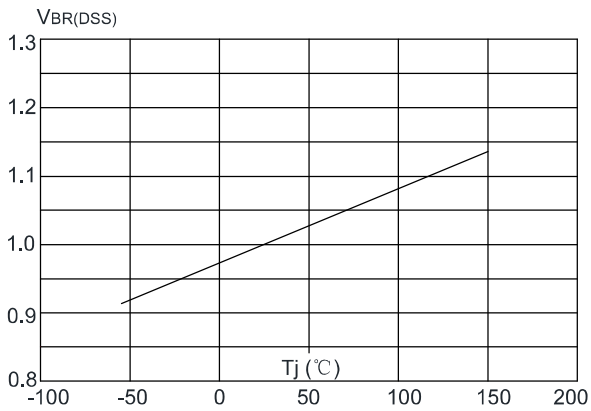


Figure 8: Normalized on Resistance vs. Junction Temperature

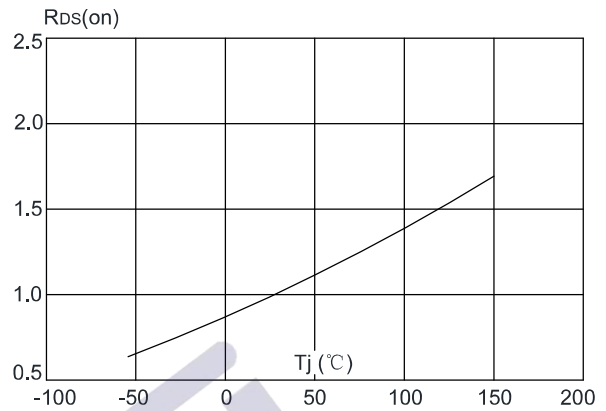


Figure 9: Maximum Safe Operating Area

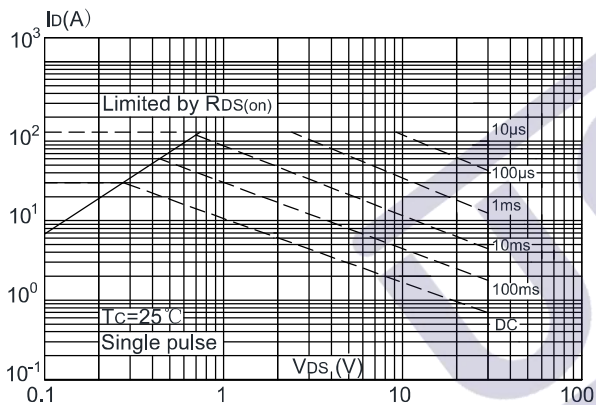


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

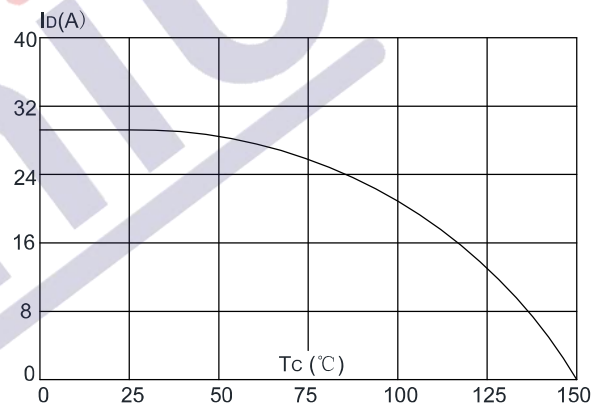
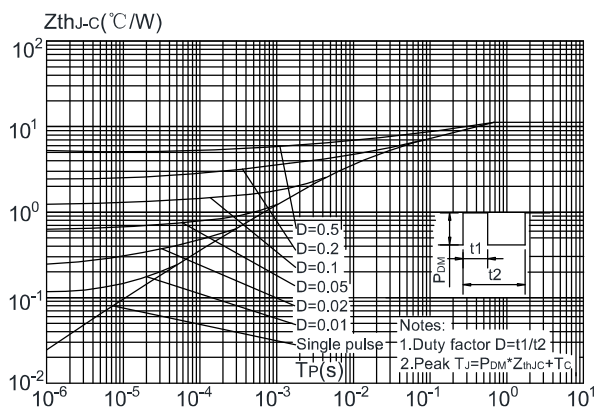
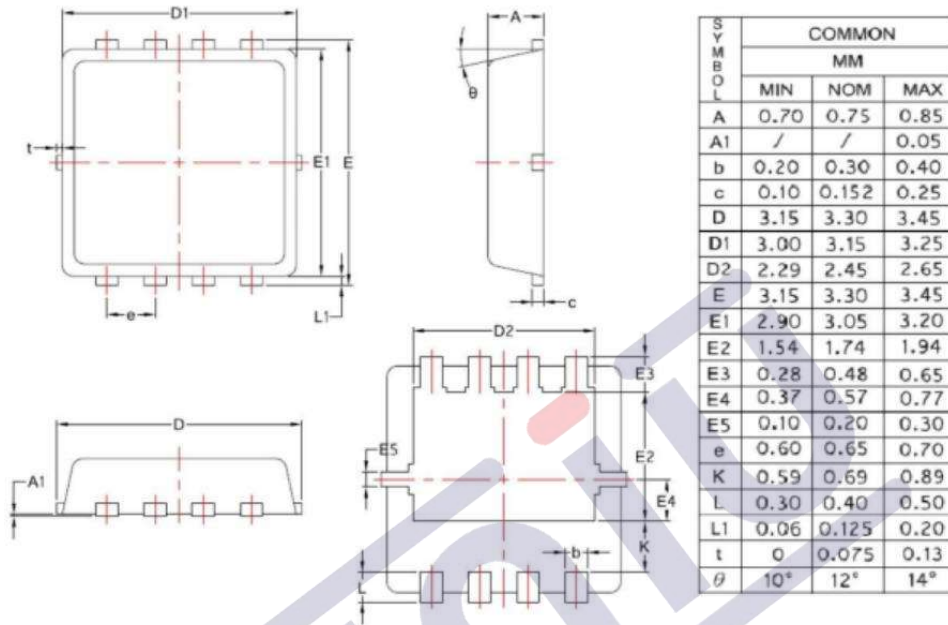


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



PDFN3X3-8L Package Information



1.版本记录

DATE	REV.	DESCRIPTION
2018/04/19	1.0	First Release
2021/11/12	1.1	Layout adjustment

2.免责声明

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